

Please AMEND the paragraph beginning at page 10, line 2, as follows:

a2
To realize the above-mentioned objective, the PDP apparatus of the present invention is provided with a sustaining circuit having phase adjusting circuits that adjust the timing of the changing edge of the sustaining pulse. By adjusting the phase adjusting circuits and optimizing the state of the timing of the changing edge of the sustaining pulse, the power recovery circuit can work efficiently and the power consumption will be reduced. In addition, since the on/off timing of the sustaining pulses applied from each sustaining circuit are optimized to each other, malfunctions or erroneous discharge can be avoided.

Please AMEND the paragraph beginning at page 10, line 14, as follows:

a3
It is particularly effective if the present invention is employed in a PDP apparatus equipped with a sustaining circuit having a power recovery circuit, or one employing an ALIS system.

IN THE CLAIMS:

Please REPLACE claims 1-10 and ADD new claims 11-16 in accordance with the following:

- Sub B1
a4
1. (ONCE AMENDED) A plasma display apparatus, comprising:
a plasma display panel equipped with first electrodes and second electrodes arranged adjacently to each other, extending in a first direction, and address electrodes extending in a second direction at a right angle to the first direction;
an X sustaining circuit that supplies sustaining pulses to said first electrodes;
a Y sustaining circuit that supplies sustaining pulses to said second electrodes;
wherein said X and Y sustaining circuits respectively comprise:
a first output device provided between a path connected to said first or second electrodes and a high potential power supply;
a second output device provided between the path connected to said first or second electrodes and a low potential power supply;
a first phase adjusting circuit that adjusts timing of a changing edge of a driving signal which drives said first output device; and
a second phase adjusting circuit that adjusts timing of a changing edge of a driving signal which drives said second output device.